

$V_{RSM}$	=	2800 V
$I_{F(AV)M}$	=	6830 A
$I_{F(RMS)}$	=	10730 A
$I_{FSM}$	=	$87 \times 10^3$ A
$V_{F0}$	=	0.8 V
$r_F$	=	0.05 mW

# Rectifier Diode

## 5SDD 60N2800

Doc. No. 5SYA1155-01 Jan. 05

- Patented free-floating silicon technology
- Very low on-state losses
- Optimum power handling capability

### Blocking

*Maximum rated values*<sup>1)</sup>

Parameter	Symbol	Conditions	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$	$f = 50$ Hz, $t_p = 10$ ms, $T_j = 160^\circ\text{C}$	2000	V
Non - repetitive peak reverse voltage	$V_{RSM}$	$f = 5$ Hz, $t_p = 10$ ms, $T_j = 160^\circ\text{C}$	2800	V

*Characteristic values*

Parameter	Symbol	Conditions	min	typ	max	Unit
Max. (reverse) leakage current	$I_{RRM}$	$V_{RRM}$ , $T_j = 160^\circ\text{C}$			400	mA

### Mechanical data

*Maximum rated values*<sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Mounting force	$F_M$		81	90	108	kN
Acceleration	a	Device unclamped			50	$\text{m/s}^2$
Acceleration	a	Device clamped			100	$\text{m/s}^2$

*Characteristic values*

Parameter	Symbol	Conditions	min	typ	max	Unit
Weight	m				2.8	kg
Housing thickness	H	$F_M = 90$ kN, $T_a = 25^\circ\text{C}$	34.3		34.9	mm
Surface creepage distance	$D_S$		56			mm
Air strike distance	$D_a$		22			mm

1) Maximum rated values indicate limits beyond which damage to the device may occur

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## On-state

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Max. average on-state current	$I_{F(AV)M}$	50 Hz, Half sine wave, $T_C = 90\text{ °C}$			6830	A
Max. RMS on-state current	$I_{F(RMS)}$				10730	A
Max. peak non-repetitive surge current	$I_{FSM}$	$t_p = 10\text{ ms}$ , $T_j = 160\text{ °C}$ , $V_R = 0\text{ V}$			$87 \times 10^3$	A
Limiting load integral	$I^2t$				$38.5 \times 10^6$	$A^2s$
Max. peak non-repetitive surge current	$I_{FSM}$	$t_p = 8.3\text{ ms}$ , $T_j = 160\text{ °C}$ , $V_R = 0\text{ V}$			$95 \times 10^3$	A
Limiting load integral	$I^2t$				$38 \times 10^6$	$A^2s$

### Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
On-state voltage	$V_F$	$I_F = 5000\text{ A}$ , $T_j = 160\text{ °C}$			1.05	V
Threshold voltage	$V_{(T0)}$	$T_j = 160\text{ °C}$ $I_T = 2500 \dots 7500\text{ A}$			0.8	V
Slope resistance	$r_T$				0.05	$m\Omega$

## Switching

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Recovery charge	$Q_{rr}$	$di_F/dt = -10\text{ A}/\mu\text{s}$ , $V_R = 200\text{ V}$ $I_{FRM} = 4000\text{ A}$ , $T_j = 160\text{ °C}$			6300	$\mu\text{As}$

## Thermal

Maximum rated values <sup>1)</sup>

Parameter	Symbol	Conditions	min	typ	max	Unit
Operating junction temperature range	$T_{vj}$				160	°C
Storage temperature range	$T_{stg}$		-40		175	°C

### Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Thermal resistance junction to case	$R_{th(j-c)}$	Double-side cooled $F_m = 81...108$ kN			5.7	K/kW
	$R_{th(j-c)A}$	Anode-side cooled $F_m = 81...108$ kN			11.4	K/kW
	$R_{th(j-c)C}$	Cathode-side cooled $F_m = 81...108$ kN			11.4	K/kW
Thermal resistance case to heatsink	$R_{th(c-h)}$	Double-side cooled $F_m = 81...108$ kN			1	K/kW
	$R_{th(c-h)}$	Single-side cooled $F_m = 81...108$ kN			2	K/kW

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_{th i} (1 - e^{-t/t_i})$$

i	1	2	3	4
$R_{th i}$ (K/kW)	3.731	1.250	0.434	0.292
$\tau_i$ (s)	0.8113	0.1014	0.0089	0.0015

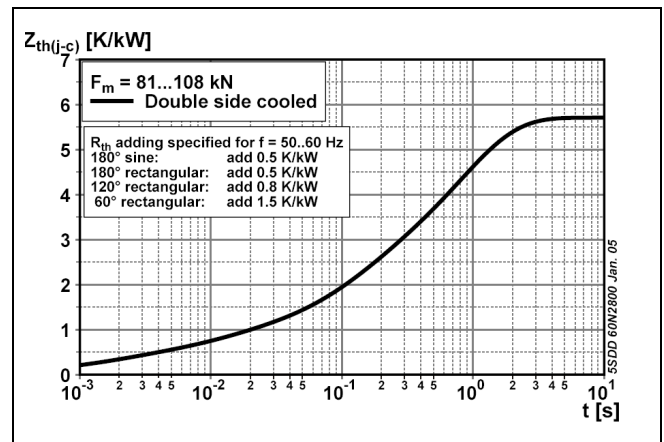


Fig. 1 Transient thermal impedance junction-to-case.

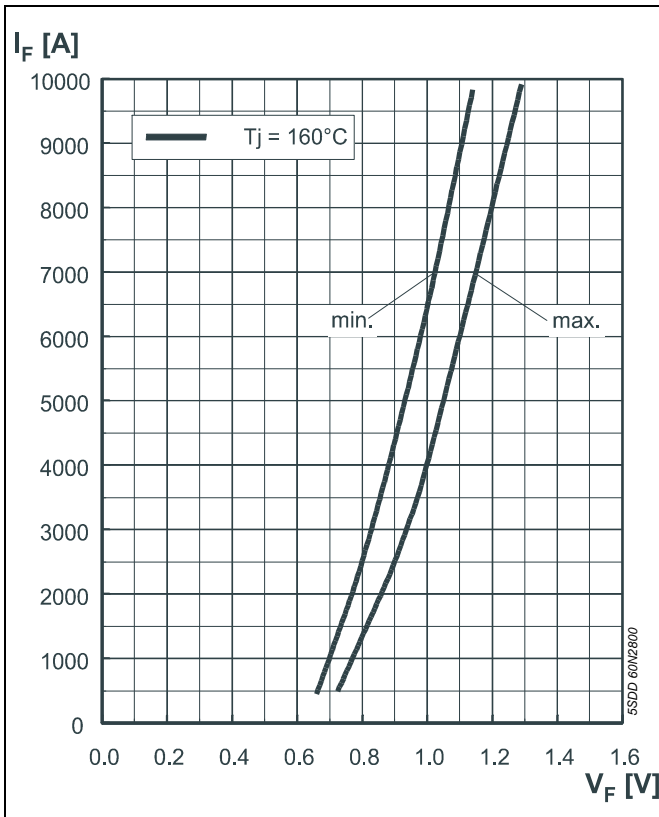


Fig. 2 On-state characteristics.

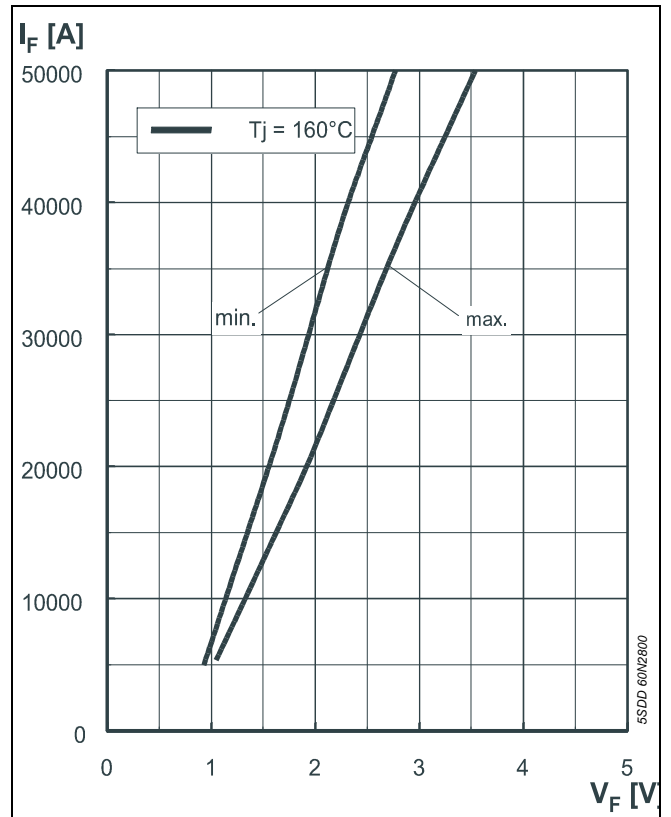


Fig. 3 On-state characteristics.

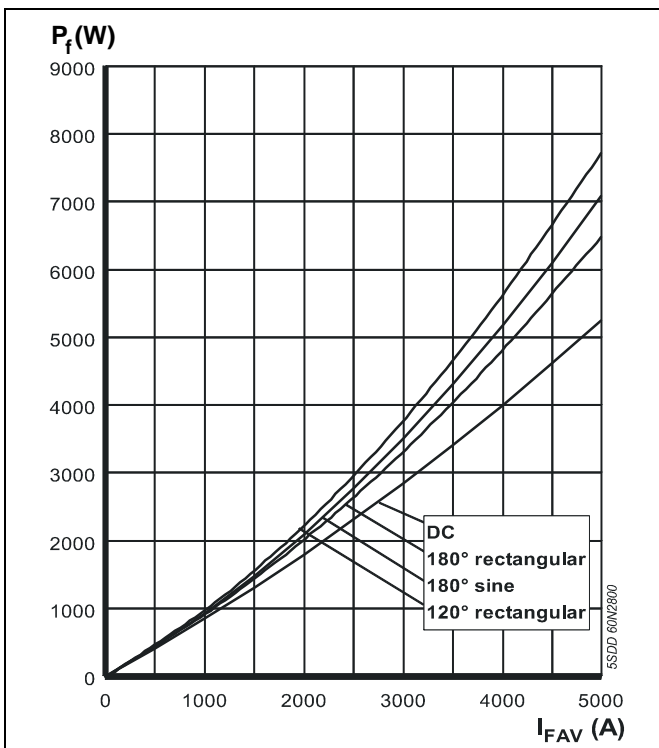


Fig. 4 On-state power losses vs average on-state current.

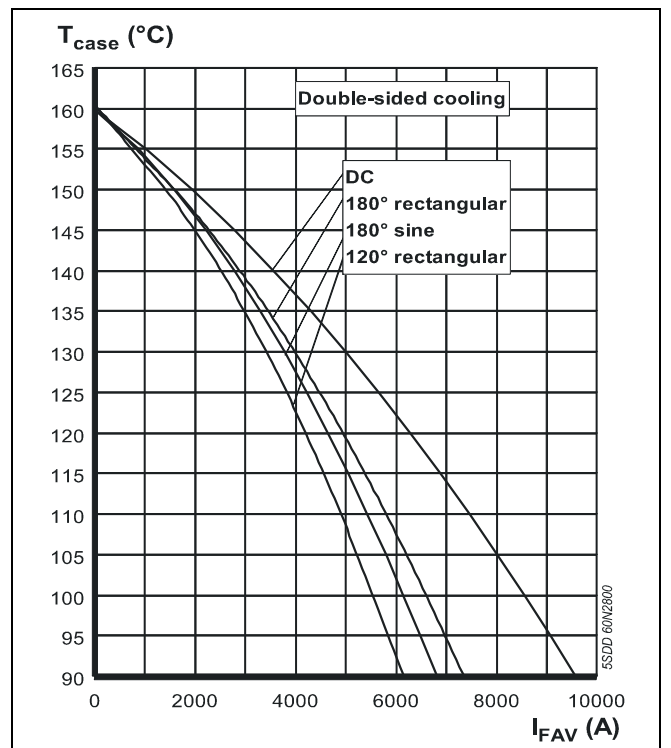
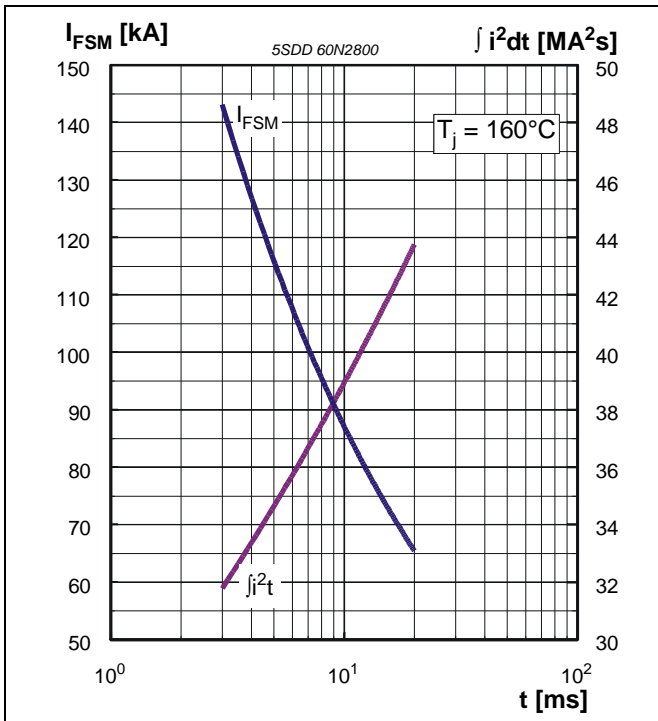
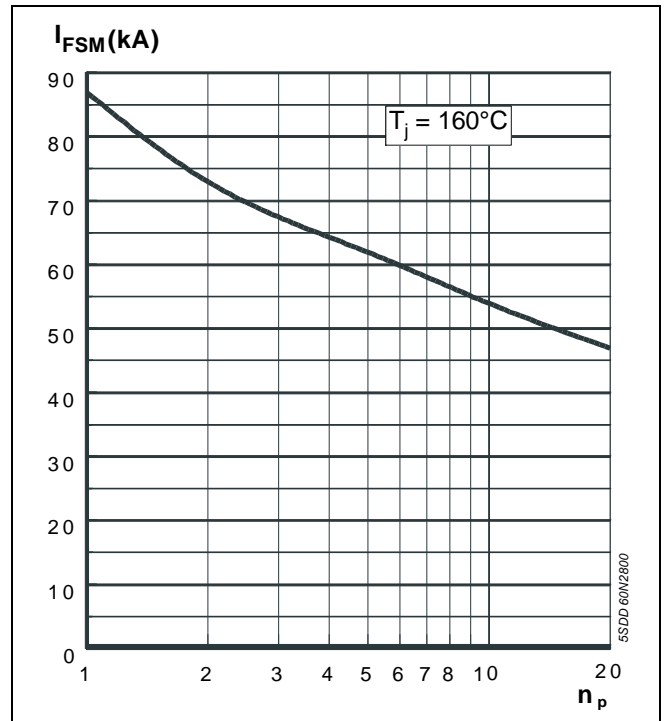


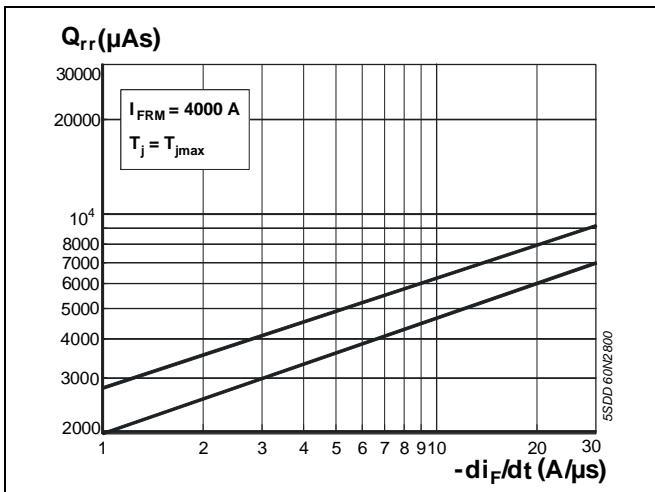
Fig. 5 Max. permissible case temperature vs average on-state current.



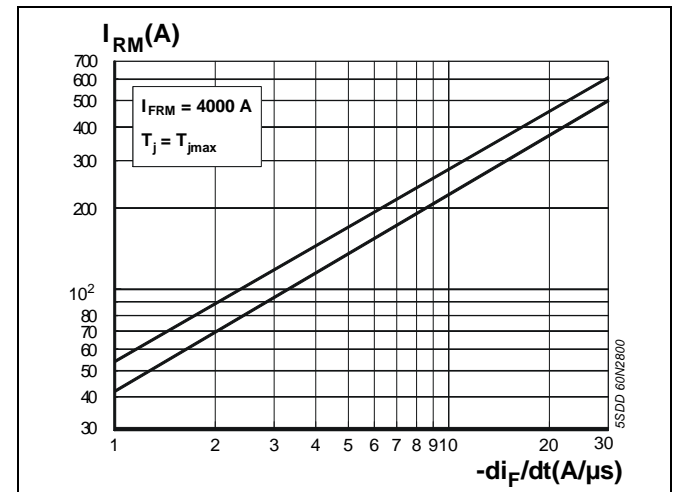
**Fig. 6** Surge on-state current vs. pulse length. Half-sine wave.



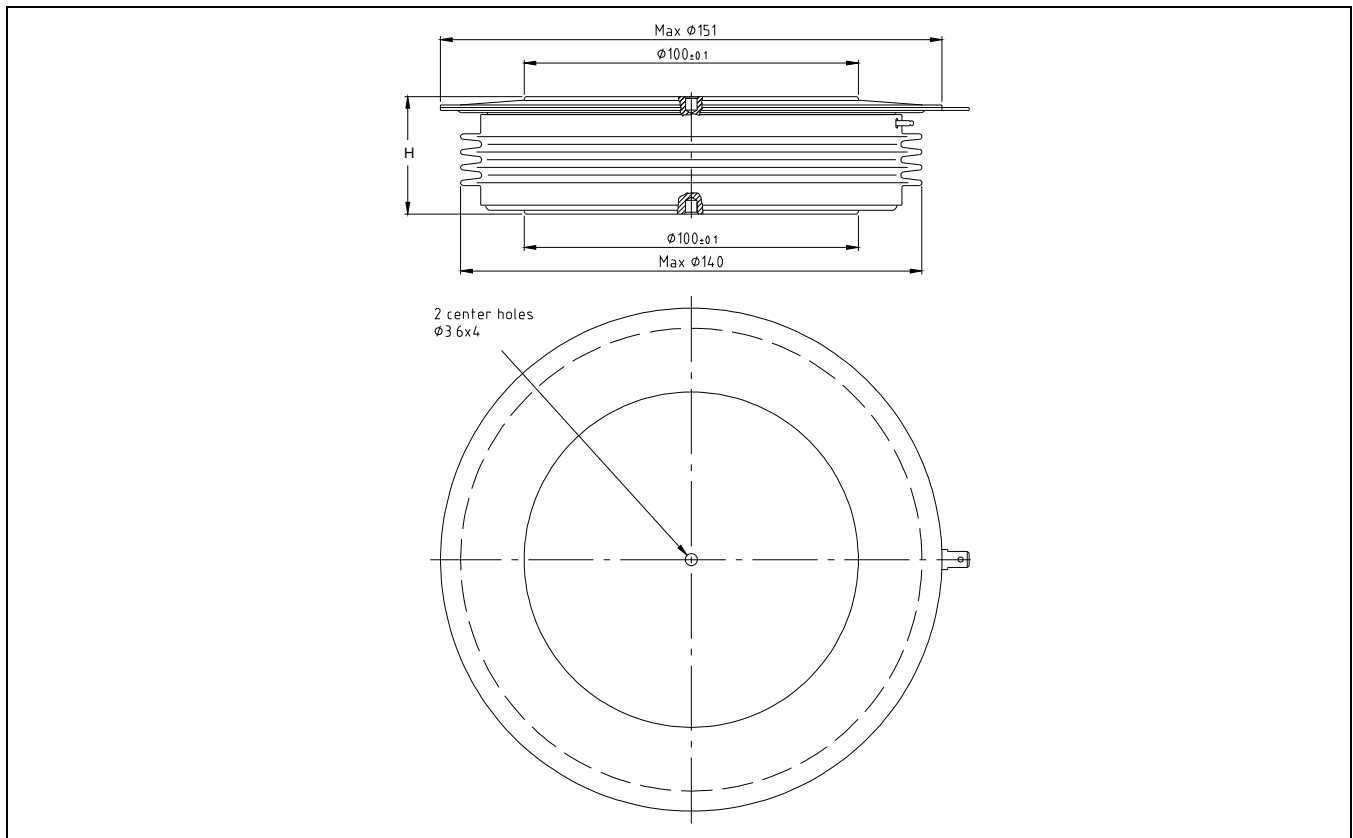
**Fig. 7** Surge on-state current vs. number of pulses. Half-sine wave, 10 ms, 50Hz.



**Fig. 8** Recovery charge vs. decay rate of on-state current.



**Fig. 9** Peak reverse recovery current vs. decay rate of on-state current.



**Fig. 10** Outline drawing. All dimensions are in millimeters and represent nominal values unless stated otherwise.

### Related application notes:

Doc. Nr	Titel
5SYA 2020	Design of RC-Snubbers for Phase Control Applications
5SYA 2029	Designing Large Rectifiers with High Power Diodes
5SYA 2036	Recommendations regarding mechanical clamping of Press Pack High Power Semiconductors

Please refer to <http://www.abb.com/semiconductors> for actual versions.

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